



Subject: PCN: V-PCN-SNG150710
Package Change for VV-800 5x3SMD VCXOs

Date: July 20, 2015

From: P. Matthews
VI-SNG Product Manager

To:

Vectron Customers:

This PCN is to inform a change to the package used in the VV-800 5x3 SMD VCXO. The package is being modified by adding external pads to the package to allow the capability for DLD and Crystal impedance (CI) testing of the crystal after the oscillator is sealed. The package will have two external plated pads added as shown below to allow for this testing and thus improve outgoing quality by screening product escapes that can potentially surface later as DLD or high impedance-related failures. Currently there is no DLD testing being performed on this part prior to shipment.

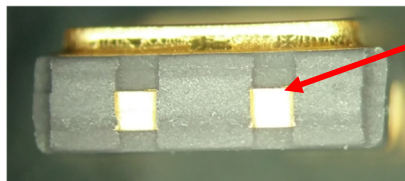
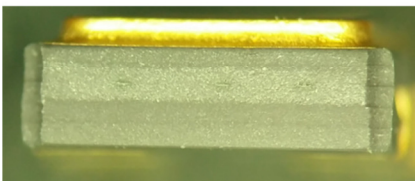
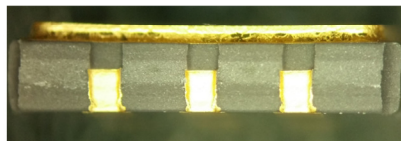
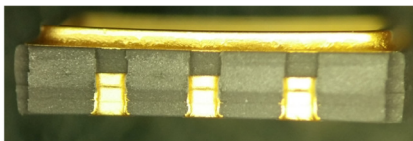
There is no change to the oscillator internal components such as the IC or the crystal and the assembly process, and therefore there will not be any change to the product performance specifications.

The use of external pads on the ceramic packages is already employed on all Vectron's XO and VCXO SMD product such as the 7x5 XOs (e.g. C-type series, VCC1-series, VCC-6 series) and VCXOs (VX-705, VV-700 etc.) and some 5x3 VCXOs (such as VX-805). Therefore this change to the VV-800 package change will standardize the package design and use of DLD pads across all of Vectron's SMD oscillator packages.

The change results in addition of the external pads and therefore it is a change to package form/appearance but the 5x3 outline is still the same as the original.

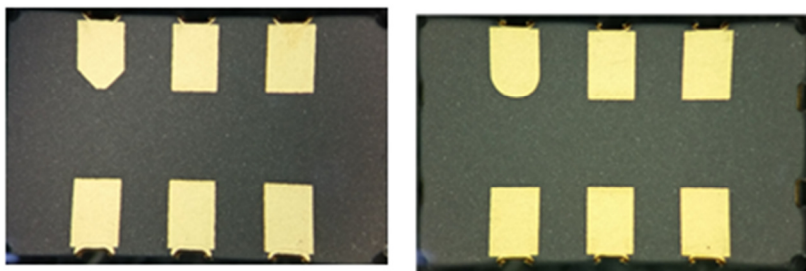
Current Package

New DLD package



Side pads on the short side of the package to allow probing of the crystal nodes

APPENDIX C



Vectron has qualified the change using oscillators built in the production line with the old and the new package and full electrical characterization.

Products affected by this change: All “VV-800” Series, VV-800-XXX series

Change Implementation date: Beginning September 2015

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